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APPLICATION NO.	J	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/775,927	09/775,927 02/02/2001		Hisaya Ishihara	NECN 18.304	3865
26304	7590	12/28/2004		EXAM	INER
		N ZAVIS ROSENM	KIM, KEVIN		
575 MADIS NEW YOR			ART UNIT	PAPER NUMBER	
	, -			2634	
				DATE MAILED: 12/28/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

•	Application No.	Applicant(s)					
	09/775,927	ISHIHARA, HISAYA					
Office Action Summary	Examiner	Art Unit					
	Kevin Y Kim	2634					
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address					
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO  - Extensions of time may be available under the provisions of 37 CFf after SIX (6) MONTHS from the mailing date of this communication  - If the period for reply specified above, the maximum statutory pe  - Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the meanned patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may a r I reply within the statutory minimum of thir riod will apply and will expire SIX (6) MON atute, cause the application to become AE	reply be timely filed  ty (30) days will be considered timely.  ITHS from the mailing date of this communication.  BANDONED (35 U.S.C. § 133).					
Status							
1) $\boxtimes$ Responsive to communication(s) filed on $\underline{0}$	9-02-2004.						
	This action is non-final.						
• • • • • • • • • • • • • • • • • • • •	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4)⊠ Claim(s) <u>1-9</u> is/are pending in the application	n.						
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.	_						
6)⊠ Claim(s) <u>1,8 and 9</u> is/are rejected.							
7)⊠ Claim(s) <u>2-7</u> is/are objected to.							
8) Claim(s) are subject to restriction ar	nd/or election requirement.						
Application Papers							
9) The specification is objected to by the Exan	niner						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to		•					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the	•	• • •					
Priority under 35 U.S.C. § 119							
<u> </u>	eion priority under 35 U.S.C. 8	\$ 119(a)-(d) or (f)					
·	12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:  1. Certified copies of the priority documents have been received.						
· <u> </u>							
2. Certified copies of the priority docum		application No.					
3. Copies of the certified copies of the							
application from the International Bu	•	Č					
* See the attached detailed Office action for a		received.					
Attachment(s)	•						
1) Notice of References Cited (PTO-892)	4) Interview S	Summary (PTO-413)					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(	s)/Mail Date					
<ol> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB Paper No(s)/Mail Date</li> </ol>	(/08) 5)	nformal Patent Application (PTO-152) 					

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### **DETAILED ACTION**

## Response to Arguments

1. Applicant's arguments filed September 2, 2004 have been fully considered but they are not persuasive.

Claim 1 has been amended to further include the limitation "said frequency conversion block including a single second frequency divider for dividing said oscillation frequency by a factor of two to generate a divided frequency." But this further limitation is still short of patentably distinguishing the present invention from the prior art, since the frequency conversion block described in the prior art (shown in Fig. 5 of the present application) also includes a single second frequency divider (310) for dividing said oscillation frequency (fosc) by a factor of two to generate a divided frequency (1/2 fosc). According to the Remarks concurrently filed, applicant apparently intended to define the frequency conversion block to include only one single frequency divider as opposed to two frequency dividers. However, because of the claim language using the transitional work "including" the claim does not exclude a QAM that includes more than one frequency divider. Amendment of the claim consistent with the Remark is suggested.

Claim 8 has been amended to include "using N frequency mixers" and applicant asserts that this amendment differs from the prior art where N=1. However, since the claim fails to define "N" to exclude the case where N=1, the prior art still reads on the claimed invention.

2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

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3. Claims 1, 8 and 9 are rejected under 35 U.S.C. 102(a) as being anticipated by an admitted prior art.

Consider claims 1 and 8. The admitted prior art, depicted in Figs. 3 and 5 and described at pages 3-8 shows a quadrature modulator/method comprising:

- 1) a local oscillator (402) whose frequency  $f_{osc}$  is "4/(2N+1) times," i.e., 4/3 times where N=1, of the carrier frequency  $f_{out}$ ,
- 2) a frequency conversion block (301 and 250) for multiplying the oscillating frequency by (2N+1)/2," i.e., 3/2 where N=1, see that the output of the frequency multiplier (250) is 3/2 f<sub>osc</sub>
- 3) a first frequency divider (240) for dividing the output of the frequency conversion block by two to generate a pair of carrier waves, see the two output carrier waves from the frequency divider (240)
- 4) first and second multipliers (210 and 220) for modulating the two carrier waves with a digital baseband signal (101) and
- 5) an adder (230) for adding the two modulated carrier waves to output a digital carrier signal f<sub>out</sub>.

Next consider claim 9. The admitted prior art, depicted in Figs. 3 and 5 and described at pages 3-8 shows a quadrature modulator/method comprising:

- 1) a digital signal generator (101),
- 2) a local oscillator (402) whose frequency  $f_{\rm osc}$  is "4/(2N+1) times," i.e., 4/3 times where N = 1, of the carrier frequency  $f_{\rm out}$ ,

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- 3) a frequency conversion block (301 and 250) for multiplying the oscillating frequency by (2N+1)/2," i.e., 3/2 where N=1, see that the output of the frequency multiplier (250) is 3/2 f<sub>osc</sub>
- 4) a first frequency divider (240) for dividing the output of the frequency conversion block by two to generate a pair of carrier waves, see the two output carrier waves from the frequency divider (240)
- 5) first and second multipliers (210 and 220) for modulating the two carrier waves with a digital baseband signal (101) and
- 6) an adder (230) for adding the two modulated carrier waves to output a digital carrier signal fout

wherein the frequency conversion block (301 and 250) includes a bandpass filter (330) and the output signal from the bandpass filter is supplied (via frequency multiplier 250) to the said first frequency divider (240).

#### Allowable Subject Matter

4. Claims 2-7 are allowed.

#### Conclusion

5. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO Application/Control Number: 09/775,927 Page 5

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MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Y Kim whose telephone number is 571-272-3039. The examiner can normally be reached on

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date of this final action.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 571-272-3056. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

kvk

CHIEH M. FAN PRIMARY EXAMINER